	Application No.	Applicant(s)
Notice of Allowability	10/827,291	KATOH ET AL.
	Examiner	Art Unit
	Anh Q. Tran	2819
The MAILING DATE of this communication apper All claims being allowable, PROSECUTION ON THE MERITS IS (herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIG of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this ap or other appropriate communicatior GHTS. This application is subject to	plication. If not included
1. This communication is responsive to 4/20/04.		
2. The allowed claim(s) is/are 27-31.		
3. ☑ Acknowledgment is made of a claim for foreign priority una) ☑ All b) ☐ Some* c) ☐ None of the:		
1. Certified copies of the priority documents have been received.		
2. Certified copies of the priority documents have been received in Application No. <u>09/582,327</u> .		
3. Copies of the certified copies of the priority documents have been received in this national stage application from the		
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of noted below. Failure to timely comply will result in ABANDONMI THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	of this communication to file a reply ENT of this application.	complying with the requirements
4. A SUBSTITUTE OATH OR DECLARATION must be submit INFORMAL PATENT APPLICATION (PTO-152) which give	tted. Note the attached EXAMINER is reason(s) why the oath or declara	'S AMENDMENT or NOTICE OF tion is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.		
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached		
1) hereto or 2) to Paper No./Mail Date	,	·
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	Amendment / Comment or in the C	office action of
Identifying indicla such as the application number (see 37 CFR 1.4 each sheet. Replacement sheet(s) should be labeled as such in the	84(c)) should be written on the drawir ne header according to 37 CFR 1.121(c	ngs in the front (not the back) of d).
 DEPOSIT OF and/or INFORMATION about the depose attached Examiner's comment regarding REQUIREMENT F 	sit of BIOLOGICAL MATERIAL r	nust be submitted. Note the
Attachment(s) 1. ☑ Notice of References Cited (PTO-892)	5 □ Notice of Informal P	atent Application (PTO-152)
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)	6. ☐ Interview Summary	
	Paper No./Mail Dat	
 Information Disclosure Statements (PTO-1449 or PTO/SB/08 Paper No./Mail Date 4/20/04; 2/14/06 1\30\06 	8), 7. ☐ Examiner's Amendn	nent/Comment
Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. X Examiner's Stateme	ent of Reasons for Allowance
o. Diologica iviateria	9. Other	

Application/Control Number: 10/827,291 Page 2

Art Unit: 2819

DETAILED ACTION

Allowable Subject Matter

1. Claims 27-31 are allowed.

2. The following is an examiner's statement of reasons for allowance: with respect to claim 27, in addition to other limitations in the claim, the prior art fails to teach, disclose or render obvious the applicant's invention as claimed, particularly the feature describing wherein ratio number of the first logic gates included in the first circuit block to number of all logic gates in the first circuit block is higher than ratio number of the first logic gates included in the second circuit block to number of all logic gates in the second circuit block.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

- 3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.
- 4. Usami (5,818,256) discloses a combinational logic circuit having a plurality of low threshold gates and a plurality of high threshold gates, but no teach of a two circuit blocks each having a plurality of logic gates and second logic gates with different threshold voltage transistors and no ratio number between the first logic gates of the first circuit block and the second circuit block.

Art Unit: 2819

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Anh Q. Tran whose telephone number is 571-272-1813. The examiner can normally be reached on M-F (8:00-5:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rexford Barnie can be reached on 571-272-7492. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

ANH Q.TRAN PRIMARY EXAMINER

6/15/06